First-order logic with reachability for infinite-state systems

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Abstract
First-order logic with the reachability predicate (FO[R]) is an important means of specification in system analysis. Its decidability status is known for some individual types of infinite-state systems such as pushdown (decidable) and vector addition systems (undecidable).

This work aims at a general understanding of which types of systems admit decidability. As a unifying model, we employ valence systems over graph monoids, which feature a finite-state control and are parameterized by a monoid to represent their storage mechanism. As special cases, this includes pushdown systems, various types of counter systems (such as vector addition systems) and combinations thereof. Our main result is a characterization of those graph monoids where FO[R] is decidable for the resulting transition systems.

Categories and Subject Descriptors F.1.1 [Models of Computation]: Automata

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1. Introduction
In the context of model checking, proving correctness of a system amounts to proving that a model of the system’s behaviour, often given in the form of an automaton, complies with a specification, typically defined by a formula in a logic over the configuration graph of the automaton model. To obtain an automated correctness proof, care needs to be taken in choosing the expressivity of the automaton model and of the logic:

First-order logic with reachability (FO[R]) can uniformly specify a wide range of correctness properties. Safety specifications can be expressed with a simple existential quantification: Can some error configuration be reached? Violations of liveness specifications can be found by checking the existence of a loop that avoids desirable configuration be reached? Violations of liveness specifications can be found by checking the existence of a loop that avoids desirable behaviour, that is, by solving a recurrent reachability problem, also expressible in FO[R].

More generally, quantifier alternation provides a useful middle ground between FO and R. In general, quantifier alternation provides a means to specify how the system should react to an environment: No matter what the choices of the environment are (∃), the system should be able to react (3) in a way that guarantees some objective is met. Moreover, FO[R] can provide a useful middle ground between first-order logic without reachability and monadic second-order logic: The latter significantly increases expressivity but at the cost of being decidable for fewer models.

Despite its importance in system analysis, the decidability status of FO[R] for infinite state systems is only known for a handful of prominent models. Using Cauca’s interpretation technique, positive results have been shown for the (higher-order) pushdown automata family (Cauca 1996; Carayol and Wühle 2003). In the parallel setting, there are positive results for PA (Lugiez and Schnoebelen 2004) and vector addition systems with states (VASS) of dimension two (Leroux and Sutre 2004). For general VASS (equivalently, Petri Nets), close to no flavour of first-order logic (even without reachability) is decidable (Darondeau et al. 2011). However, to the best of our knowledge, there is no systematic account of decidability of FO[R] for general classes of storage mechanisms. The question we address in this paper is:

Which features of the storage mechanism of the automaton model determine the decidability status of FO[R]?*

In order to investigate this question, we need a unifying framework that encompasses and generalizes all the aforementioned infinite state models. We thus set out to study FO[R] on the configuration graphs of valence systems: Finite state automata with auxiliary storage, where the storage mechanism is specified via a monoid. In this model, a configuration of a valence system over a monoid M consists of a pair (q, m) where q is a control state and m is the storage content, an element of M. A transition (q, r, q′) has the effect of multiplying r ∈ M (to the right) to the current storage content. In order to model actions that are not allowed at the current storage content (such as popping the wrong stack symbol), we restrict the configurations to right-invertible monoid elements. As an example, a stack can be modeled by the monoid whose elements can be represented by sequences of non-barred (a) and barred (ā) versions of each stack symbol (representing a push and a pop respectively) with the rule āa = 1. Then, the right-invertible elements are the sequences over non-barred symbols. A counter over N with increment and decrement (which we call a partially blind counter) can be similarly modeled by having only one stack symbol. Correspondingly, a blind counter is a counter over Z, represented by also having āa = āā.

Most importantly, new storage mechanisms can be built by composing monoids using two kinds of products. The direct product M × N of two monoids M and N allows the automaton to use the two storage mechanisms independently. This permits, for example, building counter systems of any finite dimension. The free product M ∗ N amounts, roughly speaking, to forming stacks whose entries alternate between elements of M and elements of N. A valence system over M ∗ N can push elements of N or M into the stack, manipulate the top entry using the relevant storage mechanism (N or M) and pop the top of the stack when empty.

To systematize our study, we consider the so-called graph monoids — monoids that are defined by graph presentations. Formally, a graph Γ induces a quotient MΓ over a free monoid: Each
Graph $\Gamma$ | Monoid $M\Gamma$ | Storage mechanism
---|---|---
$\bullet$ | $B^{(3)}$ | Stack (of 3 symbols)
$\bullet \bullet$ | $B^3$ | Three partially blind counters
$\triangle$ | $Z^3$ | Three blind counters
$\triangle$ | $B^{(2)} \times B^3$ | Stack (of two symbols) and three partially blind counters
$\bullet + B^3$ | $B^{(2)} \times B^3$ | Stack of three partially blind counters
$\triangle$ | $B^{(2)} \times B^{(2)}$ | Two stacks (of two symbols each)

Table 1. Examples of storage mechanisms

node represents a pair of dual letters $a$ and $\bar{a}$, which always cancel out (i.e. $a\bar{a} \equiv \varepsilon$) — in other words, nodes represent copies of what is known in the literature as the *bicyclic monoid* $B$. Edges represent a commutativity relation used to quotient the monoid. If two nodes $u$ and $v$ are adjacent, the letters they represent commute with each other, i.e. $x_u x_v \equiv x_v x_u$ for $x_w \in \{a_w, \bar{a}_w\}$, $w \in \{u, v\}$. If a node has a self loop, the two dual letters it represents commute, i.e. $a\bar{a} \equiv a\bar{a}$. A partially blind counter is thus represented by a graph with one node and no edges; a blind counter is realized by a graph with one node with a self loop. Table 1 shows several examples of storage mechanisms that can be modeled as graph monoids.

**Contributions** Our main result is a full characterization of the storage mechanisms $M\Gamma$ that admit decidability of FO[$R$], based on a simple graph-theoretic property of $\Gamma$. We call a graph a $B^2$-triangle if it is one of the graphs in Fig. 1. A graph is said to be $B^2$-triangle-free if it does not contain a $B^2$-triangle as an induced subgraph. In Theorem 2.1 we prove that requiring $\Gamma$ to be a disjoint union of cliques, where each clique is $B^2$-triangle-free, is a sufficient and necessary condition for decidability of FO[$R$].

To establish our decidability results, we employ a strong notion of automaticity for monoids, which implies automaticity of the resulting transition systems, including the reachability predicate. To be more precise, we define a notion of automatic rational multiplication — multiplication in the monoid by elements of a rational subset being representable by a synchronous relation on a regular encoding. We then characterize the shape of storage mechanisms satisfying our graph-theoretic condition as the closure under free products of the monoids $B \times Z^k$ for $k \in \mathbb{N}$ and $B \times B$. To show that, in the base case, $B \times Z^k$ and $B \times B$ have automatic rational multiplication, we establish Presburger-definability of the reachability relation. In the induction step, we give a direct construction for deriving automatic rational multiplication in $M \ast N$, assuming $M, N$ have automatic rational multiplication.

For the undecidability results, we first show that the decomposition into cliques is needed. If $\Gamma$ is not a disjoint union of cliques, then $M\Gamma$ contains $(a, b)^* \times \{e\}^*$ as a submonoid of right-invertible elements, which, as we show, offers enough structure to make the $\Sigma_1$ fragment of FO[$R$] undecidable. On the other hand, if $\Gamma$ decomposes into cliques but is not $B^2$-triangle-free, then we show how to interpret the $\Sigma_1$ theory of arithmetic with addition and multiplication, $(\mathbb{N}, +,-)$, in the $\Sigma_0$ fragment of FO[$R$] on valence systems. In both cases, undecidability already holds for a fixed valence system.

One notable consequence of our result is that FO[$R$] is decidable for $d$-dimensional VASS if and only if $d \leq 2$.

**Outline** In Section 2 we recall the basic notions around valence systems and state our main theorem. Sections 3 and 4 are devoted to proving the decidability and the undecidability results, respectively.

### 1.1 Related Work

A variety of positive results have been developed for the pushdown family. Notably, the Caucal hierarchy (Caucal 1996) admits a decidable MSO theory, via MSO-interpretations and graph unfoldings. The configuration graphs in the Caucal hierarchy are the ones generated by higher-order pushdown automata (Carayol and Wührle 2003), which therefore enjoy a decidable MSO theory. Walukiewicz (Walukiewicz 1996) obtained upper complexity bounds on $\mu$-calculus model checking for pushdown systems. The interpretation idea has been generalized by Colcombet (2002) to regular ground tree rewriting. MSO is also known to be decidable on trees generated by higher-order recursion schemes (Ong 2006). Schulz (2010) showed, using an interpretation-based approach, that FO[$R$] is decidable over a class of grid-like structures. Beyond such iterated interpretation ideas, we are not aware of results that target general storage mechanisms. While the storage mechanisms we consider are quite general, higher-order features cannot be captured by graph monoids: Higher-order pushdown systems have decidable MSO, but all graph monoids over which valence systems have decidable MSO generate only ordinary pushdown graphs.

In the concurrent setting, variants of first-order theories with reachability have been shown decidable by Lugiez and Schnoebelen (2000) for PA processes. For Petri nets, a thorough study of plain first-order logic without reachability showed that the logic is undecidable even for very weak predicates (Darondeau et al. 2011). Crucially, this holds only when one restricts the structures to the *reachable* configurations: when the unrestricted structure is considered, Petri net configuration graphs are automatic. For our setting, the restriction does not make a difference since the presence of the reachability predicate would allow us to restrict or relativize quantifiers within the logic.

Ideas akin to automaticity are also applied in the area of regular model checking (Abdulla et al. 2004). There, the goal is to approximate the transitive closure of the transition relation, using widenings and acceleration-based techniques on regular transducers. Instead of studying decidability, termination is enforced at the price of precision, obtaining constructions that are useful in practice.

Valence automata over graph monoids have recently been used to study other computational properties. For example, there is a full characterization of those graph monoids that guarantee semilinear Parikh images (Buckheister and Zetzsche 2013) and partial characterizations of those with decidable reachability (Zetzsche 2015) and of those where $\varepsilon$-transitions can be eliminated (Zetzsche 2013).

### 2. Concepts and Main Result

A *finite automaton* is a tuple $(X, Q, q_0, E, F)$ where $X$ is a finite alphabet, $Q$ is a finite set of *states*, $q_0 \in Q$ is the *initial state*,...
We represent storage mechanisms as monoids. Let $M$ be a monoid; we denote multiplication in the monoid by juxtaposition, and the neutral element by 1. The elements of $M$ are actions that can be applied to the storage content and the elements of

$$R_1(M) := \{x \in M \mid \exists y \in M : xy = 1\}$$

correspond to valid storage contents. The elements of $R_1(M)$ are said to be right-invertible. A valence system over $M$ is a tuple $S = (Q, M, E)$, in which (i) $Q$ is a finite set of states, (ii) $E$ is a finite subset of $Q \times M \times Q$, called the set of edges. A configuration of $S$ is a pair $(q, r) \in Q \times R_1(M)$. For $p, q \in Q$ and $r, s \in R_1(M)$, we write $(p, r) \rightarrow_{S} (q, s)$ if there is an edge $(p, m, q) \in E$ such that $s = rm$. The reflexive transitive closure of $\rightarrow_{S}$ is denoted $\rightarrow_{S}^\ast$.

**Graphs**

A graph is a pair $\Gamma = (V, E)$ where $V$ is a finite set and $E$ is a subset of $\{\{v, w\} \mid v, w \in V\}$. The elements of $V$ are called vertices and those of $E$ are called edges. Vertices $v, w \in V$ are adjacent if $\{v, w\} \in E$. If $\{v\} \in E$ for some $v \in V$, then $v$ is called a looped vertex, otherwise it is unlooped. A subgraph of $\Gamma$ is a graph $(V', E')$ with $V' \subseteq V$ and $E' \subseteq E$. Such a subgraph is called induced (by $V'$) if $E' := \{S \in E \mid S \subseteq V'\}$, i.e. $E'$ contains all edges from $E$ between vertices from $V'$. By $\Gamma \setminus \{v\}$, for $v \in V$, we denote the subgraph of $\Gamma$ induced by $V \setminus \{v\}$. Moreover, a clique is a graph in which any two distinct vertices are adjacent. Finally, $\Gamma^−$ denotes the graph obtained from $\Gamma$ by deleting all self-loops: We have $\Gamma^− := (V, E^−)$, where $E^− := \{S \in E \mid |S| = 2\}$. Finally, a graph is transitive if for $v, x, y, z \in V$, pairwise distinct, we have that $\{x, y\}, \{y, z\} \in E$ implies $\{x, z\} \in E$.

**Graph monoids**

Let $A$ be a (not necessarily finite) set of symbols and $R$ be a subset of $A^\ast \times A^\ast$. The pair $(A, R)$ is called a (monoid) presentation. The smallest congruence of $A^\ast$ containing $R$ is denoted $\equiv_R$ and we will write $[w]_R$ for the congruence class of $w \in A^\ast$. The monoid presented by $(A, R)$ is defined as $A^\ast / \equiv_R$. Note that since we did not impose a finiteness restriction on $A$, up to isomorphism, every monoid has a presentation. Furthermore, for monoids $M_1, M_2$ we can find presentations $(A_1, R_1)$ and $(A_2, R_2)$ such that $A_1 \cap A_2 = \emptyset$. We define the free product $M_1 * M_2$ to be presented by $(A_1 \cup A_2, R_1 \cup R_2)$. Note that $M_1 * M_2$ is well-defined up to isomorphism. In analogy to the $n$-fold direct product, we write $M^{(n)}$ for the $n$-fold free product of $M$.

With each graph $\Gamma = (V, E)$, we associate the alphabet of generators $X_\Gamma := \{a_v, \bar{a}_v \mid v \in V\}$, and the smallest congruence $\equiv_{\Gamma}$ satisfying

$$a_v \equiv_{\Gamma} \bar{a}_v \equiv_{\Gamma} v$$

for each $v \in V$, and

$$xy \equiv_{\Gamma} yx$$

for each $x \in \{a_v, \bar{a}_v\}$,

$$y \in \{a_w, \bar{a}_w\} \text{ with } \{v, w\} \in E.$$

In particular, we have $a_v \equiv_{\Gamma} \bar{a}_v \equiv_{\Gamma} a_v a_w$ whenever $\{v\} \in E$. With each graph $\Gamma$, we associate the monoid $M_{\Gamma} := X_\Gamma / \equiv_{\Gamma}$. The monoids of the form $M_{\Gamma}$ are called graph monoids.

Let us briefly discuss how to realize storage mechanisms by graph monoids. First, suppose $\Gamma_0$ and $\Gamma_1$ are disjoint graphs. If $\Gamma$ is the union of $\Gamma_0$ and $\Gamma_1$, then $M_{\Gamma} \cong M_{\Gamma_0} \ast M_{\Gamma_1}$ by definition. Moreover, if $\Gamma$ is obtained from $\Gamma_0$ and $\Gamma_1$ by drawing an edge between each vertex of $\Gamma_0$ and each vertex of $\Gamma_1$, then $M_{\Gamma} \cong M_{\Gamma_0} \times M_{\Gamma_1}$.

If $\Gamma$ consists of one vertex $v$ and has no edges, the only congruence rule is $a_v \bar{a}_v \equiv_{\Gamma} v$. In this case, $M_{\Gamma}$ is also denoted $\mathbb{B}$ and we will refer to it as the *bicyclic monoid*. The generators $a_v$ and $\bar{a}_v$ are then also written $a$ and $\bar{a}$, respectively. Observe that $R_1(\mathbb{B}) = \{a\}^\ast$ and thus $R_1(\mathbb{B}) \cong \mathbb{N}$. Multiplying $a$ or $\bar{a}$ on the right represents increment or decrement, respectively. Since $a$ and $\bar{a}$ do not commute, $\mathbb{B}$ corresponds to a *partially blind counter*, i.e. one that attains only non-negative values. For instance, starting from $a$, we cannot decrement twice and add one by multiplying $\bar{a}a$ because $a\bar{a}a \equiv_{\mathbb{B}} \bar{a} \not\in R_1(\mathbb{B})$.

Moreover, if $\Gamma$ consists of one looped vertex, then $M_{\Gamma}$ (and also $R_1(\mathbb{M}_{\Gamma})$) is isomorphic to $\mathbb{Z}$ and thus realizes a *blind counter*, which can go below zero.

If one storage mechanism is realized by a monoid $M$, then the monoid $\mathbb{B} * M$ corresponds to the mechanism that builds stacks: A configuration of this new mechanism consists of a sequence $c_0, a_1, \ldots, a_n$, where $c_0, \ldots, c_n$ are configurations of the mechanism realized by $M$. We interpret this as a stack with the entries $c_0, \ldots, c_n$. One can open a new stack entry on top (by multiplying $a \in \mathbb{B}$, remove the topmost entry if empty (by multiplying $\bar{a} \in \mathbb{B}$) and operate on the topmost entry using the old mechanism (by multiplying elements from $M$). In particular, $\mathbb{B} * \mathbb{B}$ describes a pushdown stack with two stack symbols. Observe that $R_1(\mathbb{B} * \mathbb{B})$ is isomorphic to the monoid of words over two symbols, i.e. stack words. Table 1 shows a few more examples. See (Zetsche 2013) for details.

**Logic**

Let $S$ be a valence system over the monoid $M$. Then the first-order logic with reachability for $S$, $\text{FO}[R(S)]$, consists of the first-order formulas over the following signature:

- The *constant symbols* are the configurations of $S$.
- $\text{state}_q(\cdot)$: A unary *state predicate* for each $q \in Q$.
- $\text{step}(\cdot, \cdot)$: The binary *step predicate* for one-step reachability.
- $\text{reach}(\cdot, \cdot)$: The binary *reachability predicate*.

Given a valence system $S$, its *reachability structure* is the relational structure with domain $Q \times R_1(M)$ over the above signature defined in the obvious way. For a first-order formula $\varphi$, we write $S \models \varphi$ when $\varphi$ holds on the reachability structure of $S$. The *first-order theory with reachability over $S$* is then the set of formulas $\{\varphi \in \text{FO}[R(S)] \mid \varphi$ closed, $S \models \varphi\}$.

### 2.1 Main Result

We call $\Gamma$ a $\mathbb{B}^2$-triangle if it is one of the graphs of Fig. 1. A graph $\Gamma$ is said to be $\mathbb{B}^2$-triangle-free if it does not contain a $\mathbb{B}^2$-triangle as an induced subgraph.

**Theorem 2.1.** Let $\Gamma$ be a graph. The first-order theory with reachability for valence systems over $M_{\Gamma}$ is decidable if and only if $\Gamma$ is a disjoint union of $\mathbb{B}^2$-triangle-free cliques.

Before we move to the proof, let us elaborate on the concrete shape of the graphs satisfying the condition of Theorem 2.1. If $\Delta$ is a $\mathbb{B}^2$-triangle-free clique, then $\Delta$ either (i) consists of at most two unlooped vertices or (ii) contains at most one unlooped vertex and a number of looped vertices. Since all these vertices are adjacent, this means we either have $\mathbb{M}_{\Delta} \cong \mathbb{B}^2$ or $\mathbb{M}_{\Delta} \cong \mathbb{B}^2 \times \mathbb{Z}^{s}$ for $r \in \{0, 1\}$ and $s \in \mathbb{N}$. If $\Gamma$ is a disjoint union of $\mathbb{B}^2$-triangle-free cliques $\Delta_1, \ldots, \Delta_n$, then $M_{\Gamma}$ is the free product of the $M_{\Delta_i}$.

Operationally, the storage mechanism implemented by such an $M_{\Gamma}$ is composed of a stack, each entry of which is either (i) a pair of partially blind counters, or (ii) a partially blind counter with a number of blind counters.
3. Decidability

In this section, we show that $\forall \mathcal{R}[\mathcal{B}]$ is decidable for valence systems over $\mathcal{M}[\mathcal{G}]$ whenever $\mathcal{G}$ is a disjoint union of $\mathcal{B}^2$-triangle-free cliques. More specifically, we prove that the transition systems—with the reachability relation—generated by such systems over $\mathcal{M}[\mathcal{G}]$ are effectively automatic. We deduce this property from a slightly more abstract condition on the monoid, which we call automatic rational multiplication.

As we argued in the previous section, if $\mathcal{G}$ is a disjoint union of $\mathcal{B}^2$-triangle-free cliques, then $\mathcal{M}[\mathcal{G}]$ is isomorphic to the free product of a finite number of monoids, each of which is isomorphic either to $\mathcal{B}^2$ or to $\mathcal{B}^2 \times \mathcal{Z}^k$, for $r \in \{0,1\}$ and $s \in \mathbb{N}$. Therefore, we proceed as follows:

(i) We show that $\mathcal{B} \times \mathcal{Z}^n$ has automatic rational multiplication for each $n \geq 0$. Here, we use a direct construction showing that the reachability relation for two fixed states is Presburger-definable and can thus conclude automaticity.

(ii) We observe that $\mathcal{B} \times \mathcal{B}$ has automatic rational multiplication. Note that $\mathcal{R}_1(\mathcal{B} \times \mathcal{B}) \cong \mathbb{N} \times \mathbb{N}$ and that valence systems over $\mathcal{B} \times \mathcal{B}$ are essentially two-dimensional vector addition systems with states. We can therefore apply a result of Leroux and Sutre (2004), stating that the binary reachability relation of such systems is effectively semilinear. Specifically, given a rational subset $R$ of $\mathcal{B} \times \mathcal{B}$, one can easily construct a 2-dim. VASS whose binary reachability relation for two states is precisely all those $((x,y),(x',y'))$ for which there is an $r \in R$ with $(x,y) r = (x',y')$. Since this semilinearity implies Presburger-definability, we directly obtain an encoding with automatic rational multiplication.

(iii) We obtain a general transfer result, stating that if two monoids $M_0$ and $M_1$ each have automatic rational multiplication, then so does their product product $M_0 \times M_1$. Here, we use a generalization of the saturation method for pushdown automata (Benois 1969; Bouajjani et al. 1997) to obtain automaticity.

Hence, for the decidability, it remains to prove (i) and (iii), which we do in Sections 3.2 and 3.3, respectively. We begin with the required concepts.

3.1 Automaticity

Let $X$ be an alphabet and $\circ \notin X$. We define the alphabet $X(2,\circ)$ as $(X \cup \{\circ\})^2 \setminus \{(\circ,\circ)\}$ and the convolution $u \circ v \in X(2,\circ)^*$ of words $u, v \in X^*$ inductively as follows. We have

\[ au \circ bv = (a,b)(u \circ v), \quad \varepsilon \circ \varepsilon = \varepsilon \]
\[ \varepsilon \circ bv = (\varepsilon \circ v) \quad au \circ \varepsilon = (a,\varepsilon)(u \circ \varepsilon) \]

for $a, b \in X$ and $u, v \in X^*$. For a relation $R \subseteq X^* \times X^*$, we write $R^\circ = \{u \circ v \mid (u,v) \in R\}$. Such a relation is called regular if $R^\circ$ is a regular language.

Let $(D,R_1,\ldots,R_n)$ be a relational structure with domain $D$ and relations $R_1,\ldots,R_n$ of arities $r_1,\ldots,r_n \leq 2$ (for our purposes, it suffices to consider arities $\leq 2$). This structure is called automatic if there is a regular language $L \subseteq X^*$, and a bijection $\theta : L \rightarrow D$ such that each of the relations

\[ R_i^\theta = \{x_1,\ldots,x_{r_i} \in L \mid \theta(x_1),\ldots,\theta(x_{r_i}) \in R_i\} \]

is regular. The first-order theory of each automatic structure is decidable (Khoussainov and Nerode 1995), even when extended by the infinity quantifier (Blumensath and Grädel 2000) and the modulo quantifier (Khoussainov et al. 2004).

We will show that in the decidable case of Theorem 2.1, the reachability structure of each such valence system is (effectively) automatic. Here, it will be convenient to prove a more abstract condition, termed 'automatic rational multiplication', which implies the desired automaticity. We say that a monoid $M$ is finitely generated if there is an alphabet $X$ and a surjective morphism $[\cdot] : X^* \rightarrow M$. The image of $w \in X^*$ under $[\cdot]$ will be denoted $[w]$ and we write $[K] = \{[w] \mid w \in K\}$ when $K \subseteq X^*$. Note that if $M$ is finitely generated, we can denote elements of $M$ by words over $X$. From now on, we assume that $M$ is finitely generated and fix $X$ and $[\cdot]$. While the following definitions make reference to $X$ and $[\cdot]$, it is easy to see that they do not depend on this choice. A subset $R \subseteq M$ is called rational if there is a regular language $K \subseteq X^*$ such that $R = [K]$. We will represent such a rational subset of $M$ by a finite automaton for $K$.

Definition 3.1. Let $M$ be finitely generated. An encoding for $M$ is a bijection $\theta : L \rightarrow R_1(M)$ where $L$ is a regular language. We say that $\theta$ has automatic rational multiplication if for each rational subset $R \subseteq M$, the relation

\[ R^\circ = \{(u,v) \in L \times L \mid \exists r \in R : \theta(u)r = \theta(v)\} \]

is effectively regular, i.e. one can compute a finite automaton for $(R^\circ)^\circ$. In this case, we also say that $M$ has automatic rational multiplication.

Note that here, the alphabet of $L$ is usually not $X$.

Theorem 3.2. Suppose $M$ has an encoding with automatic rational multiplication. Then each valence system over $M$ has an effectively automatic reachability structure.

The proof of Theorem 3.2 is not difficult. One has to extend the encoding with the states of a given valence system. One then observes that the configurations in state $q$ reachable from $(p,m)$ are precisely those of the form $(q,nr\cdot \varepsilon)$, where $r$ is drawn from a rational set. Hence, the reachability relation is regular. Moreover, encodings of step relation and configurations follows from the special case where the rational set is finite.

3.2 Base Case $\mathcal{B} \times \mathcal{Z}^k$

We show that $\mathcal{B} \times \mathcal{Z}^k$ has automatic rational multiplication. The key observation is that a rational set over this monoid behaves like a 1-counter automaton (1CM) over $\mathcal{Z}$ that is decorated with further counters over $\mathcal{Z}$. Decorated means only the $\mathcal{N}$-counter influences the behavior of the automaton, the $\mathcal{Z}$-counters do not.

To prove automaticity, we show that the reachability relation in such decorated 1CM is Presburger definable. Automaticity then follows from automaticity of Presburger arithmetic. To establish Presburger definability of the reachability relation, we make use of semilinearity of the Parikh images of 1CM languages, combined with the equivalence of semilinear and Presburger-definable sets.

Proposition 3.3. $\mathcal{B} \times \mathcal{Z}^k$ has automatic rational multiplication.

The monoid $\mathcal{B} \times \mathcal{Z}^k$ is finitely generated by definition. Since we have an isomorphism $\mathcal{R}_1(\mathcal{B} \times \mathcal{Z}^k) \cong \mathbb{N} \times \mathcal{Z}^k$, we can identify the elements of $\mathcal{R}_1(\mathcal{B} \times \mathcal{Z}^k)$ with pairs $(m,\vec{c}) \in \mathbb{N} \times \mathcal{Z}^k$. Consider a rational set $R \subseteq \mathcal{B} \times \mathcal{Z}^k$. We have to characterize the set of pairs $(m,\vec{c})$ and $(n,\vec{d})$ for which there is an element $r \in R$ so that $(m,\vec{c})r = (n,\vec{d})$. In the latter situation, $(n,\vec{d})$ is said to be reachable from $(m,\vec{c})$. More precisely, we choose a regular language $L$ where each word $w \in L$ encodes an element $\theta(w)$ of $\mathbb{N} \times \mathcal{Z}^k$. Then, we show that the set of pairs $(u,v) \in L \times L$ such that $\theta(u)$ is reachable from $\theta(v)$ is synchronous rational. Fortunately, we can express reachability of $(n,\vec{d})$ from $(m,\vec{c})$ in Presburger arithmetic, for which a suitable encoding is already available. Presburger arithmetic is the first-order logic over $(\mathbb{Z}, \leq, +)$. 


Lemma 3.4. Given a rational set $R \subseteq \mathbb{B} \times \mathbb{Z}^k$, one can construct a Presburger formula $\varphi_R(x_1, y_1, x_2, y_2)$ so that for all $(m, \varepsilon)$ and $(n, \delta)$ from $\mathbb{N} \times \mathbb{Z}^*$ we have

$$\exists r \in R: (m, \varepsilon)r = (n, \delta) \iff \varphi_R(m, \varepsilon, n, \delta)$$

Before we turn to the construction, we explain why the result guarantees the encoding requirement. The structure $(\mathbb{Z}, +, \leq)$ has a first-order interpretation in $(\mathbb{N}, +, \leq)$, the more common formulation of Presburger arithmetic. (One idea is to provide two variables over the naturals for each integer). Automaticity of Presburger arithmetic over $\mathbb{N}$ goes back to (Büchi 1960). To be precise, there is a bijection between the naturals and the elements in a regular language. Moreover, there are automata for the encodings of solutions to Presburger-definable relations. Automaticity is preserved under first-order interpretations (Blumensath and Grädel 2000). A direct encoding of $(\mathbb{Z}, +, \leq)$ into automata (sign followed by most-significant bit) is given in (Boigelot and Wolper 1998). Finally, the set $\mathbb{N} \times \mathbb{Z}^*$ is first-order definable, meaning we can find an encoding of precisely the elements of $R_4(\mathbb{B} \times \mathbb{Z}^*)$.

To compute the Presburger formula $\varphi_R$, we develop an understanding of the rational set $R \subseteq \mathbb{B} \times \mathbb{Z}^*$ as a 1CM. By definition, the rational set is represented by a finite automaton $N$ over letters say $a$ and $\bar{a}$ for $\mathbb{B}$ and $b_i$ and $\bar{b}_i$ for each copy of $\mathbb{Z}$, $i = 1, \ldots, k$. We interpret $\mathbb{B}$ and $\mathbb{Z}$ as counters (partially blind and blind, respectively). With this point of view, $a$ and $\bar{a}$ similarly $b_i$ and $\bar{b}_i$ are increment and decrement operations on the counters that is accepted by $N$. The formula $\varphi_R$ is supposed to capture the pairs $(m, \varepsilon)$, yields $(n, \delta)$. The only requirement on this application is that the $\mathbb{N}$-counter is never decremented below zero. However, this is precisely the semantics of a 1CM over $\mathbb{N}$. We make this relationship between $N$ and 1CM explicit, and show how it leads to the desired computability result.

Let the given finite automaton be $N = (X, Q_0, q_0, E, F)$ with alphabet $X = \{a, \bar{a}, b_1, b_2, \ldots, b_k, \bar{b}_1, \ldots, \bar{b}_k\}$ and edges $E \subseteq Q \times X \times Q$. A 1CM $C$ is a finite automaton that has access to an $\mathbb{N}$-counter and where every edges carries an increment, decrement, or no-op command $(E \subseteq Q \times X \times \{\pm 1, 0\} \times Q)$. The semantics is defined as expected. We write $(q_i, m_1) \xrightarrow{a,c} (q_2, m_2)$ to mean that there is a sequence of transitions between configurations of $C$ that is labeled by $\sigma \in X^*$ and that takes $(q_i, m_1)$ to $(q_2, m_2)$. We use $L(C, m) := \{\sigma \in X^* \mid \exists q_i \subseteq Q, i \subseteq \mathbb{N}: (q_0, m) \xrightarrow{\sigma} (q_i, \varepsilon)\}$ to denote the language of $C$ from the initial counter value $m$. Our automaton $N$ induces the 1CM $C_N$ defined as follows. Edges labeled by $a$ are given command $+1$, because they correspond to increments on the one $\mathbb{N}$-counter. Edges labeled by $\bar{a}$ are given $-1$. The remaining edges are decorated with $0$. Note that the labeling stays unchanged, and that we do not introduce commands for the $\mathbb{Z}$-counters. To state the relationship between $N$ and $C_N$, we need the Parikh image of words $w \in X^*$. The Parikh image of $w$ is the function $\psi(w): X \rightarrow \mathbb{N}$ where $(\psi(w))\langle \alpha \rangle$ is the number of occurrences of the letter $\alpha$ in the word $w$.

Lemma 3.5. There is an $r \in R$ so that $(m, \varepsilon)r = (n, \delta)$ if and only if there is $a \in L(C, m)$ so that

$$y = x + (\psi(a))(z) - (\psi(a))(\bar{z})$$

for all $(x, y, z) \in \{(m, n, a), (c_1, d_1, b_1), \ldots, (c_k, d_k, b_k)\}$.

The lemma rephrases the existence of $r \in R$ as the existence of a $C_N$-computation. The main message is that the effect on the counters can be deduced from the Parikh image of the computation. This holds true even for the $\mathbb{N}$-counter. Nevertheless, we need the semantics of a 1CM (rather than all computations of the finite automaton $N$) to make sure the $\mathbb{N}$-counter stays non-negative also in all intermediary configurations.

Lemma 3.5 can be read as follows. For a fixed value $m$, it characterizes the suitable $\varepsilon, \delta$, and $n$ in terms of the Parikh images of the computations of $C_N$. To give a characterization for all $m$, we guess the initial value of the $\mathbb{N}$-counter. Let $C_N$ coincide with $C_N$ except for a fresh initial state $q_0$, and a fresh letter $\bar{a}$. The new state carries a loop $(q_0, a, +1, q_0)$ and has an $\varepsilon$-transition to the former initial state. $(q_0, \varepsilon, 0, q_0)$.

Lemma 3.6. There is $r \in R$ so that $(m, \varepsilon)r = (n, \delta)$ if and only if there is $w \in L(C_{N'}, 0)$ so that

$$m = (\psi(w))\langle \bar{a} \rangle$$

$(n, \delta) = (\psi(w))\langle \bar{a} \rangle + (\psi(w))(a) - (\psi(w))(\bar{a})$ and $d_i = c_i + (\psi(w))(b_i) - (\psi(w))(\bar{b}_i)$ for all $i = 1, \ldots, k$.

Since $C_N$ is an ordinary 1CM, its language is context-free. With Parikh’s theorem (Parikh 1966), the Parikh image of this language is a semilinear set, and hence Presburger definable. The equations in the lemma explain how to turn a Presburger formula for $\psi(L(C_{N'}, 0))$ into a formula for our set of pairs.

3.3 Induction Step

In this section, we show that the property of having automatic rational multiplication is passed on to free products. This means, we prove the following:

Theorem 3.7. If the monoids $M_0$ and $M_1$ have automatic rational multiplication, then so does $M_0 \ast M_1$.

Let us briefly sketch the proof steps. First, we devise a saturation procedure for rational sets (Lemma 3.9). This allows us to assume that in the automata over $M_0 \ast M_1$, every element can be accepted through a word in a normal form. This normal form is amenable to an analysis of how elements can cancel during multiplication (Lemma 3.10). Then, we use Lemma 3.10 to show that for each rational set, the set of encodings of its members and that of their inverses is effectively regular (Lemma 3.12). Finally, we use this and the possible cancelation cases from Lemma 3.10 again to construct a regular relation for each of these cases.

Let $M_0$ and $M_1$ be monoids with automatic rational multiplication. Then, $M_0$ and $M_1$ are finitely generated, so assume alphabets $X_0$ and $X_1$ with $X_0 \cap X_1 = \emptyset$ and a surjective morphism $[\iota]: X^*_0 \rightarrow M_0$, for each $i \in \{0, 1\}$. For the monoid $M = M_0 \ast M_1$, we pick the surjective morphism $[\iota]: X^* \rightarrow M$ with $X = X_0 \cup X_1$ that extends $[\iota]^0$ and $[\iota]^1$.

By our assumption, for each $i \in \{0, 1\}$, there is an encoding $\theta_i: L_i \rightarrow R_1(M_i)$ with automatic rational multiplication. It is an easy exercise to show that we may assume $\varepsilon \in L_i$ and $\theta_i(\varepsilon) = 1$. We may also assume $Y_0 \cap Y_1 = \emptyset$, where $Y_i$ is the finite alphabet of the language $L_i$ in our encoding for $M_0 \ast M_1$, we take $Y = Y_0 \cup Y_1$ as the alphabet and $L = (L_0 \cup L_1)^*$. As the regular language. In order to define $\theta: L \rightarrow R_1(M_0 \ast M_1)$, we need some terminology. Let $w \in X^*$ and $i \in \{0, 1\}$. A non-empty factor $f$ of $w$ is called an i-block (or block of type i) if $f \in X^*_i$ and has no neighboring symbol in $X_i$. It is called a block if it is an i-block for some $i \in \{0, 1\}$. The unique decomposition $w = w_1 \cdots w_n$ where each $w_j$ is a block of $w$ is called $w$’s block decomposition. Note that since also $Y = Y_0 \cup Y_1$ and $Y_0 \cap Y_1 = \emptyset$, we may apply the terms block and block decomposition analogously for words over $Y$. We call a word $w \in X^*$ reducible if for some $i \in \{0, 1\}$, it has a block $f$ with $|f| = 1$. Otherwise, $w$ is called reduced. The following follows easily from the definition of the free product.
Fact 3.8. Suppose $u$ and $v$ are reduced and have block decompositions $u = u_1 \cdots u_n$ and $v = v_1 \cdots v_m$. Then $[u] = [v]$ if and only if $m = n$ and $[u_i] = [v_i]$ for every $i \in \{1, \ldots, n\}$.

We are now ready to define the map $\theta: L \to R_3(M)$. It is well-known (and can be deduced from Lemma 3.10) that $R_3(M_0 \ast M_1) = R_3(M_0) \ast R_3(M_1)$. For $w \in L_i$, $i \in \{0, 1\}$, we define $\theta(w) = 0_i(w)$. Recall that we have $L_0 \cap L_1 = \{\epsilon\}$, but this causes no contradiction because the neutral elements of $M_0$ and $M_1$ are identified in $M_0 \ast M_1$. For the general case, suppose $w \in L$ with block decomposition $w = w_1 \cdots w_n$. Then we have $w_i \in L_0 \cup L_1$ for $j \in \{1, \ldots, n\}$ and we set $\theta(w) := \theta(w_1) \cdots \theta(w_n)$. Observe that $\theta$ is surjective because $\theta_0$ and $\theta_1$ are. Moreover, it follows from Fact 3.8 that $\theta$ is injective.

Rational subsets of $M = M_0 \ast M_1$ are represented by finite automata over $X$. Of course, such an automaton might accept words that are not reduced. However, we will see that we can always construct an automaton for $R$ that is saturated, meaning that $[L(A)] = R$ and whenever $m \in [L_0(A)]$ for states $p, q$, then there is a reduced word $w \in L_0(A)$ with $[w] = m$.

Lemma 3.9. For each rational subset $R \subseteq M = M_0 \ast M_1$, we can compute a saturated automaton for $R$.

Here, the idea is to introduce an $e$-edge between $p$ and $q$ whenever $1 \in [L_0(A)]$. If we do this until we reach a fixpoint, we arrive at a saturated automaton.

Proof. We are given $R$ as a finite automaton $A = (X, Q, q_0, E, F)$ such that $[L(A)] = R$. We define, for states $p, q \in Q$, the finite automaton $A_{pq}$ as $(X, Q, \{p, q\}, E, \{\epsilon\})$. In other words, we take $A$ and designate $p$ and $q$ as initial and (only) final state, respectively.

First, observe that if $N$ is a monoid with automatic rational multiplication, then given a rational subset $R \subseteq N$, it is decidable whether $1 \in R$: We have $1 \in R$ if and only if $(1, 1) \in R^0$, which we can check because $(R^0)^\omega$ is effectively regular.

We apply a saturation procedure, in which we successively add edges to $E$. We begin with $E_0 = E$ and assume we have constructed $E_i$. Consider the automaton $A_i = (X, Q, q_0, E_i, F)$. We check whether there is a pair $(p, q)$ of states such that there is no edge $(p, e, q)$ and we have $1 \in [L_0(A_i)]$. This is decidable by our observation since $[L_0(A_i)]$ is rational. If we find such a pair of states, we set $E_{i+1} = E_i \cup \{(p, e, q)\}$. If there is no such pair, the procedure terminates.

We clearly have $E_0 \subseteq E_1 \subseteq \cdots$ and since we add no states to the automaton, the procedure has to terminate with some $E_k$. We claim that then, $A_k$ has the desired property.

First of all, it is clear that in each step, we have $[L(A_i)] = [L(A_{i+1})]$. The inclusion $\subseteq$ holds trivially and the other inclusion holds by the choice of $p$ and $q$. In particular, we have $[L(A_k)] = R$.

Now let $p, q \in Q$ and $m \in [L_0(A_k)]$ and choose a word $w \in L_0(A_k)$ with $[w] = m$ such that $w$ has a minimal number of blocks. Suppose $w$ is not reduced, i.e. $w = ufv$ with a block $f$ such that $|f| = 1$. By construction of $A_k$, this means $uv \in L_0(A_k)$. However, since $[uv] = [w]$ and $w$ has at least one block less than $w$, this contradicts the choice of $w$. Hence, $w$ must be reduced. $\square$

For our proof, we need to understand how multiplication works in $M_0 \ast M_1$ in terms of reduced words. We identify four cases, the simplest being the “non-merging” case. Let $u = u_1 \cdots u_n$ and $v = v_1 \cdots v_m$ be block decompositions of $u, v \in X^*$. We call $u$ and $v$ non-merging if $u_n$ and $v_1$ are blocks of distinct types. Otherwise, we say that $u$ and $v$ are merging. Of course, when $u$ and $v$ are non-merging, then $uv$ is again a reduced word and represents $[u][v]$. As above, we see that $u_0 \cap Y_1 = \emptyset$, the notion of merging words also applies to encodings, i.e. members of $L$. Note that if $x$ and $y$ are non-merging and $x, y \in L$, then $xy \in L$ and $\theta(xy) = \theta(x)\theta(y)$.

We now want to understand multiplication in the merging case. For a proof of the following lemma, see Appendix B.

Lemma 3.10. Let $u, v, w \in X^*$ be reduced and let $u$ and $v$ be merging; moreover, let $w = w_1 \cdots w_k$ be the block decomposition of $w$. Then we have $[uw] = [w]$ if and only if one of the following holds:

(i) We have $m \leq n$ and
   (a) for every $j \in \{1, \ldots, m\}$, we have $[u_{m-j+1}v_j] = 1$,
   (b) $k = n - m$, and
   (c) $[v_1 \cdots v_k] = [v_{n+1} \cdots v_n]$.

(ii) We have $m > n$ and
   (a) for every $j \in \{1, \ldots, n\}$, we have $[u_{m-j+1}v_j] = 1$,
   (b) $k = m - n$, and
   (c) $[v_1 \cdots v_k] = [u_1 \cdots u_k]$.

(iii) There is an $i \in \{0, \min(m, n)\}$ such that
   (a) for every $j \in \{1, \ldots, i\}$, we have $[u_{m-j+1}v_j] = 1$,
   (b) $[u_{m-i}v_{i+1}] \neq 1$,
   (c) $k = (m-i-1) + 1 + (n-i-1)$,
   (d) $[w_1 \cdots w_{m-i-1}v_i] = [u_1 \cdots u_{m-i-1}]$,
   (e) $[w_{m-i}v_i] = [u_{m-i}v_{i+1}]$,
   (f) $[w_{m-i+1} \cdots w_n] = [v_{i+2} \cdots v_n]$.

For relations $S, T \subseteq X^* \times X^*$, we define $S \cdot T = \{(x_1x_2, y_1y_2) \mid (x_1, y_1) \in S, (x_2, y_2) \in T\}$.

When constructing regular relations, we employ the following fact, which is well known and easy to see.

Fact 3.11. Let $S \subseteq X^* \times X^*$ be regular and let $C \subseteq Y^*$ and $D \subseteq Y^*$ be regular. Then, the relation $S \cdot (C \times D)$ is regular as well. Moreover, if every pair $(x, y) \in S$ satisfies $|x| = |y|$ and $T \subseteq X^* \times Y^*$ is regular, then $S \cdot T$ is also regular.

We now show that for the encoding $\theta$, the right-invertible members of a rational set have effectively regular encodings, and the same is true for their left-inverses. Let us define this precisely. Suppose $N$ is a monoid and we have an encoding $\eta: K \to R_3(N)$. Then, for rational subsets $R \subseteq N$, we define $R^e := \{w \in K \mid \eta(w) \in R\}$, $R^r := \{w \in K \mid \exists r \in R: \eta(w)r = 1\}$.

If the encoding function has automatic rational multiplication, these sets are always regular. This is because the set $R^e$ is the projection of $R^0 \cap (\{(\eta^{-1}(1)) \times X^*)$ onto the right component. Hence, $R^e$ is regular. Analogously, $R^r$ is the projection of the relation $R^0 \cap (X^* \times \{(\eta^{-1}(1))$ onto the left component. We now lift this effective regularity to our encoding for $M_0 \ast M_1$. The idea is to (i) decompose each run of an automaton for $R$ into blocks representing elements $
eq 1$ (which is possible thanks to Lemma 3.9) and (ii) replace each of them with the encoding (employing effective regularity for $M_0$ and $M_1$). In the case of $R^e$, we have to reverse the sequence of blocks.

Lemma 3.12. For each rational subset $R \subseteq M = M_0 \ast M_1$, the sets $R^e$ and $R^r$ are effectively regular.

Proof. Suppose that $R = [L(A)]$ for the finite automaton $A = (X, Q, q_0, E, F)$. By Lemma 3.9, we may assume that every element of $R$ is represented by a reduced word. Observe that it is decidable whether $1 \in R$: The only reduced word that can represent 1 in $M$ is the empty word, meaning we need to check whether $\epsilon \in L(A)$. Recall that $X = X_0 \cup X_1$. By $A_1$, we denote the automaton obtained from $A$ by deleting all edges labeled with $X_{1-i}$.

We begin with the set $R^e$. We will construct a regular language $K$ and a regular substitution $\sigma$ such that $\sigma(K) = R^e$, which
implies effective regularity of $R'$. The set $K$ is a language over $Z = Q \times \{0, 1\} \times \hat{Q}$ and consists of all non-empty words 

$$(p_0, i_1, p_1)(p_1, i_2, p_2) \cdots (p_{n-1} , i_n, p_n)$$

such that $p_0 = q_0$, $p_n \in F$, and for every $j \in [1, n - 1]$, we have $i_{j+1} = 1 - i_j$. Moreover, we add $\varepsilon$ to $K$ if and only if $1 \in R$. We will use the rational set $R_{pq} \subseteq M$, defined by $R_{pq} := [L_{pq}(A_1)]$. The substitution $\sigma : Z \to \overline{P}(Y^*)$ is defined as follows. For $(p, i, q) \in Z$, we set $\sigma(z) := R_{pq} \setminus \{\varepsilon\}$, where $R_{pq}$ is effectively regular by our observation above. One can now show that $R' = \sigma(K)$ (see Appendix C).

Let us now show regularity of $R'$. We use the language $K$ from above, but in reverse, and instead of $\sigma$, we use the substitution $\tau : Z \to \overline{P}(Y^*)$ where for $z = (p, i, q)$ we set $\tau(z) := R_{pq} \setminus \{\varepsilon\}$. Again, one can show that $R' = \tau(K_{rev})$ (see Appendix C).

We are now ready to show Theorem 3.7, i.e. regularity of $R^\circ$. Here, our strategy is to construct a regular relation for each of the four cases of a multiplication ("non-merging" and the three cases of Lemma 3.10). In order to show that the constructed relations are regular, we first introduce some auxiliary sets. Let $R = \{C(A)\}$ be a rational subset of $M = M_0 \times M_1$, where $A = (X, Q, q_0, E, F)$. We may assume that $A$ is saturated (Lemma 3.9) and that $A$ has only one final state, $F = \{f\}$. The first set we use consists of all encoding pairs that are equal and either empty or end in an $\ell$-block:

$$E_{\ell} := \{((u, u) \mid u \in \{\varepsilon\} \cup (L_0 \cup L_1)^* L_\ell\}.
$$

$E_{\ell}$ is clearly regular. In order to describe the result of canceling elements of $R$ with others, we employ the set $R_{p,q,\ell} := [L_{pq}(A_\ell)]$, which is a rational subset of $M_{\ell}$. Since we assume $\theta$ to have automatic rational multiplication, the following set is effectively regular:

$$P_{p,q,\ell} := \{((u, v) \in L_\ell \times (L_\ell \setminus \{\varepsilon\}) \mid \exists r \in R_{p,q,\ell}:
\theta(u)r = \theta(v)\}.
$$

Note that the right component is required to be non-empty, so this set is not quite the same as $R_{p,q,\ell}$, but it can be obtained from the latter by an intersection with the regular relation $L_\ell \times (L_\ell \setminus \{\varepsilon\})$ (recall that regular relations are closed under intersection (Elgot and Mezei 1965)). We also need two variants of $R_{p,q,\ell}$, namely those that pick only encodings that are empty or start (end) in an $\ell$-block:

$$I_{p,q,\ell} = R_{p,q,\ell} \cap \{\varepsilon\} \cup L_\ell (L_0 \cup L_1)^* L_\ell^*$$

$$I'_{p,q,\ell} = R_{p,q,\ell} \cap \{\varepsilon\} \cup L_\ell (L_0 \cup L_1)^* L_\ell^*$$

Finally, we use a variant of $R_{p,q,\ell}$ that requires encodings to be empty or to start in an $\ell$-block:

$$W_{p,q,\ell} = R_{p,q,\ell} \cap \{\varepsilon\} \cup L_\ell (L_0 \cup L_1)^* L_\ell^*.$$

According to Lemma 3.12, the sets in Eqs. (5) to (7) are effectively regular.

Our task is to show that the set of pairs $(u, v)$ such that there is an $r \in R$ with $\theta(u)r = \theta(v)$ is regular. To this end, we want to apply Lemma 3.10, which is, however, expressed in terms of monoid elements represented as sequences of generators and $\varepsilon$, and not via $\theta$. However, since for a word $w \in Y^*$ and a block decomposition $w = w_1 \cdots w_n$, with $w_1^* \in Y^*$, we have $\theta(w) = \theta(w_1)\theta_1^{-1}(w_2)\theta(w_3) \cdots$, the product of two encodings via $\theta$ is built according to the same pattern of cancelation and of multiplication analyzed in Lemma 3.10 for sequences of generators.

Thus, Lemma 3.10 (and the remark above it) tell us that there are four possible situations. We call these cases non-merging (if $x$ and $y$ are non-merging), suffix (case (i) of Lemma 3.10), prefix (case (ii)), and mixed (case (iii)).

The simplest case is the non-merging case, since we just have to concatenate encodings $x$ and $y$ to get $z$. This means, the two components have a common prefix (corresponding to $x$) and then the second component proceeds with the encoding of an element of $R$. Hence, these pairs are represented in the following set:

$$T_1 := \bigcup_{\ell \in \{0, 1\}} E_{\ell} \cdot \{\varepsilon\} \times W_{0,\ell,1-\ell}.$$

In the suffix case, $y$ is split up in $y = y_1y_2$ such that $x$ cancels with $y_1$, so that the result is $y_2$. Therefore, we first guess the type $\ell$ of the last block of $y_1$ and we guess the state $p$ that $A$ is in after it has read the part $y_1$. Then, in the left component, we generate $x$ as an element that can be canceled by one that is read from $q_0$ to $p$. In the right component, we generate $y_2$ as an element that is read by $A$ from $p$ to the final state:

$$T_2 := \bigcup_{\ell \in \{0, 1\}} \bigcup_{p,p' \in Q} E_{\ell} \cdot P_{p,q,\ell} \cdot W_{p',\ell,1-\ell}.$$

Now, the prefix case works similarly to the suffix case: Here, $x$ is split up as $x = x_1x_2$ such that $x_2$ cancels with $y$:

$$T_3 := \bigcup_{\ell \in \{0, 1\}} E_{\ell} \cdot (I_{0,\ell,1-\ell} \times \{\varepsilon\})$$

The mixed case is more involved. As depicted in Fig. 2, $x$ and $y$ decompose as $x = x_1x_2x_3$ and $y = y_1y_2y_3$ (which is not necessarily a block decomposition) such that (i) $x_3$ cancels with $y_1$, (ii) $x_2$ and $y_2$ are blocks of the same type $\ell$ that are multiplied as in $M_\ell$ and yield a result $\neq 1$, and (iii) the prefix $x_1$ of $x$ and the suffix $y_3$ of $y$ are passed to the result without modification. This is realized as follows. First, we guess the block type $\ell$ of $x_2$ and $y_2$ and which states the automaton $A$ is in after it reads the part $y_1(p)$ and after it reads $y_2(q)$. The relation $E_{1-\ell} \cdot I_{0,\ell,1-\ell} \cdot P_{p,q,\ell}$ generates $x_2$ on the left side and the product of $x_2$ and $y_3$ on the right side. Then, $x_3$ is described by $W_{0,\ell,1-\ell}$: Recall that $x_3$ cancels with $y_1$, which is read from $q_0$ to $p$. Finally, $y_3$ is described by $W_{0,\ell,1-\ell}$:

$$T_4 := \bigcup_{\ell \in \{0, 1\}} \bigcup_{p,p' \in Q} E_{1-\ell} \cdot P_{p,q,\ell} \cdot (I_{0,\ell,1-\ell} \times W_{0,\ell,1-\ell}).$$

By Fact 3.11, each of the relations $T_1, T_2, T_3, T_4$, and hence their union, is regular. It can be verified that $R^\circ$ equals $T_1 \cup T_2 \cup T_3 \cup T_4$. See Appendix D for a detailed proof.

4. Undecidability

We show that if $\Gamma$ is not a disjoint union of $B^3$-triangle-free cliques, the first-order theory with reachability is undecidable — even for a fixed valence system over $\mathbb{M}$. A graph is a disjoint union of cliques if and only if it is transitive. Therefore, a graph can fail to be a disjoint union of $B^3$-triangle-free cliques for two reasons: Either $\Gamma^-$ contains the graph $\text{graph}$ as an induced subgraph, or $\Gamma$ contains a $B^3$-triangle. To each of these cases we devote one section.
4.1 Undecidability: Non-Transitive Graphs

Suppose \( \Gamma^- \) contains \( \bullet \) as an induced subgraph. This means \( \Gamma^- \) contains a submonoid \((M_0 \ast M_1) \times M_2\), where each \( M_i \) is either \( \mathbb{B} \) or \( \mathbb{Z} \). In any case, \( R_1(\Gamma^-) \) contains a submonoid that is isomorphic to \( \{a, b\}^* \times \{c\}^* \). We will show that undecidability can be proved by just relying on the submonoid \( \{a, b\}^* \times \{c\}^* \); operationally, this means we will restrict ourselves to automata that never make use of barred symbols. That is, if we interpret \( a \) and \( b \) as the two symbols of a stack and \( c \) as representing a counter, the systems we will construct never perform a pop, nor a decrement. This is in sharp contrast to other questions in automata theory, where even bounding reversals yields decidability results (Ibarra 1978).

Our main finding is that we can in fact give two arguments for undecidability, one where the formula is fixed, and one for a fixed valence system.

Theorem 4.1. Assume \( \Gamma^- \) is as discussed above.

(i) There is a fixed FO\( [R] \)-formula that cannot be checked for valence systems over \( \Gamma^- \).

(ii) There is a fixed valence system over \( \Gamma^- \) with an undecidable first-order theory with reachability.

For the first claim, we reduce the following undecidable problem to ours:

**Theorem 4.2 (Sakarovitch (1992)).** Given a rational subset \( R \) of \( \{a, b\}^* \times \{c\}^* \), it is undecidable to determine whether \( R \) equals \( \{a, b\}^* \times \{c\}^* \).

Let \( R \subseteq \{a, b\}^* \times \{c\}^* \) be a rational set as recognized by the finite state automaton \( A \). The automaton \( A \) can be turned into a valence system \( S_A \) with states \( q_f \) and \( q_0 \), such that \( (q_f, m) \) is reachable from \( (q_0, 1) \) if and only if \( m \in R \). We now construct the following valence system, which we call \( S_A^+ \).

In this new system, we can use the transitions on \( s \) to reach, from \( (s, 1) \) all and only the configurations \( (t, m) \) with \( m \in \{a, b\}^* \times \{c\}^* \); moreover, from \( (q_0, 1) \) we can reach \( (t, m) \) if and only if \( m \in R \). We therefore obtain that \( S_A^+ = \forall \colon \text{state}(c) \land \text{reach}(s, 1, 1) \Rightarrow \text{reach}(t, m, 1) \) holds if and only if \( R = \{a, b\}^* \times \{c\}^* \). Note that the formula can be evaluated on any valence system having three states named \( t \), \( s \), and \( q_0 \), respectively.

To establish the second claim, we reduce a variant of Post's Correspondence Problem (PCP). The initialized PCP that we rely on is the following:

**Theorem 4.3 (Harju et al. (1996)).** There is a fixed alphabet \( X \), a fixed alphabet \( Y \), and fixed morphisms \( \alpha, \beta : X^* \rightarrow Y^* \) such that the following problem is undecidable: Given a word \( u \in Y^* \), is there a word \( w \in X^* \) satisfying \( \alpha(w) = \beta(w) \)?

We encode an instance of initialized PCP into checking a \( \Sigma_1 \) formula over a valence system. The precise statement is in the next lemma, a proof of which concludes the proof of Theorem 4.1.

**Lemma 4.4.** Take \( X, Y, \alpha, \) and \( \beta \) from Theorem 4.3. There is a fixed valence system \( S_{\alpha\beta} \) and a mapping from \( u \in Y^* \) to the \( \Sigma_1 \) formula \( \varphi_u \) so that \( S_{\alpha\beta} \models \varphi_u \) if \( \alpha(w) = \beta(w) \) for some \( w \).

\(^1\)In the original result, the problem is finding \( w \) such that \( \alpha u(w) = \beta(w) \). Our variant is equivalent via word reversal.

We give a construction that depends on \( X, Y, \alpha, \) and \( \beta \). By choosing the parameters appropriately, we arrive at the lemma. We begin with the construction of \( S_{\alpha\beta} \). The alphabets \( X \) and \( Y \) can be assumed to be disjoint. We encode their union \( X \cup Y = \{a_1, \ldots, a_n\} \) as words over the alphabet \( \{a, b\} \). (Remember that we are sure to have the submonoid \( \{a, b\}^* \times \{c\}^* \)). The encoding is via the morphism \( \gamma : (X \cup Y)^* \rightarrow \{a, b\}^* \) with \( \gamma(a_i) = ab^{i-1} \).

We now show how to represent morphisms \( \mu : X^* \rightarrow Y^* \) by rational sets. In the first step, we represent \( \mu \) as \( W_\mu \subseteq \{a, b\}^* \times \{c\}^* \).

The set encodes the pairs \( w \mu(w) \) together with information about the length of \( \mu(w) \).

In a second step, and this is the point in the definition of \( W_\mu \), we observe that the complement \( \overline{W_\mu} = \{a, b\}^* \times \{c\}^* \setminus W_\mu \) is rational and effectively computable from \( \mu \), due to Sakarovitch (1992). Intuitively, since we have negation in the logic, it will not matter whether we use \( W_\mu \) or its complement to represent \( \mu \).

An automaton recognizing \( \overline{W_\mu} \) can be turned into a valence system \( S_\mu \). To this end, we introduce distinguished states \( q_0 \) and \( t_\mu \) such that the \( t_\mu \)-configurations reachable from \( (s_\mu, 1) \) are exactly of the form \( (t_\mu, m) \) with \( m \in \overline{W_\mu} \). We can make sure \( t_\mu \) does not have outgoing edges.

We construct \( S_{\alpha\beta} \) by taking the union of \( S_\mu \) and \( S_A^\ast \) as above, and adding new states \( q_\alpha \) and \( q_\beta \) as below:

Note that \( m \in W_\alpha \) iff \( (t_\alpha, m) \) is not reachable from \( (s_\alpha, 1) \), and similar for \( (t_\beta, m) \).

We now construct the formula \( \varphi_u \) for the given word \( u \in Y^* \). To explain the idea, consider a configuration \( e_1 = (t_\alpha, (v_1, c_{11})) \) that is not reachable from \( (s_\alpha, 1) \). With the previous note, this means \( v_1 = \gamma(w\alpha(w)) \) and \( k_1 = |w\alpha(w)| \) for some \( w \in X^* \). Assume \( e_1 \) leads to \( e_2 = (t_\beta, (v_2, c_{22})) \) via a path that multiplies \( \gamma(u) \) to the store. Then we have \( v_2 = v_1 \gamma(u) = \gamma(w\alpha(w)u) \) and \( k_2 = k_1 + |u| = |w\alpha(w)u| \). The relationship with the instance of the initialized PCP problem is as follows. Configuration \( e_2 \) is not reachable from \( (s_\beta, 1) \) iff \( v_2 = \gamma(w'\beta(w')) \) and \( k_2 = |\beta(w')| \) for some \( w' \in X^* \). But since we also have \( v_2 = \gamma(w\alpha(w)u) \) and \( k_2 = |w\alpha(w)u| \), we can conclude \( w' = w \). We found a solution, namely \( \beta(w) = \alpha(w) \) to the initialized PCP instance.

Formula \( \varphi_u \) phrasers the above setting in first-order logic. It is \( \Sigma_1 \) as we only have to existentially quantify over \( e_1 \) and \( e_2 \). Assume for the moment we have a predicate path\( \alpha(u)(e_1, e_2) \) that guarantees the following. If it holds for configurations \( e_1 = (t_\alpha, (v_1, c_{11})) \) and \( e_2 = (t_\beta, (v_2, c_{22})) \), then \( v_2 = v_1 \gamma(u) \) and \( k_2 = k_1 + |u| \). With this predicate, formula \( \varphi_u \) is

\[ \exists e_1, e_2 \colon \text{state}(e_1) \land \text{reach}(s_\alpha, 1, e_1) \land \text{state}(e_2) \land \neg \text{reach}(s_\beta, 1, e_2) \land \text{path}_\alpha(u)(e_1, e_2). \]

The argumentation in the previous paragraph derives the desired equivalence in Lemma 4.4.
It remains to define the predicate \( p_{\gamma(u)}(e, c') \). Let \( \gamma(u) \) be the word \( x_1 \ldots x_n \) from \( \{a, b\}^* \). The trick is to refer to the control states \( q_a \) and \( q_b \):

\[
\exists e_1 \ldots e_{n+1} : \text{step}(e, e_1) \land \text{step}(e_{n+1}, e') \land \text{state}_{q_a}(e_{n+1}) \land \bigwedge_{i=1}^{n} \text{step}(e_{i}, e_{i+1}) \land \bigwedge_{i=1}^{n} \text{state}_{q_{e_{i}}}(e_{i}).
\]

It is readily checked that the predicate satisfies the requirements.

4.2 Undecidability: \( B^2 \)-Triangle

Suppose \( \Gamma \) contains a \( B^2 \)-triangle as an induced subgraph. Then, \( \mathit{MT} \) contains \( B^2 \times \mathbb{Z} \) or \( B^2 \) as a submonoid. In any case, it contains the monoid \( B \times \mathbb{B} \times \mathbb{N} \) as a submonoid. By making use of this substructure, we construct a valence system \( S_0 \) over \( \mathit{MT} \) in which the structure \( (\mathbb{N}, +, \cdot) \) of the natural numbers with addition and multiplication can be interpreted. The \( \Sigma_2 \) fragment of arithmetic with addition and multiplication is undecidable (Matiyasevich 1993), which, as we show, implies undecidability of the \( \Sigma_2 \) fragment over \( \mathit{MT} \).

**Theorem 4.5.** For the (fixed) valence system \( S_0 \) constructed below, checking the \( \Sigma_2 \) fragment of \( \mathit{FO}[R] \) is undecidable.

To derive the undecidability, it suffices to interpret the structure \( (\mathbb{N}, +, \cdot) \) with addition and squaring \((n \mapsto n^2)\). This is because the identity \( 2ab = (a+b)^2 - a^2 - b^2 \) allows us to express multiplication in terms of squaring and addition. In the remainder of the section we thus show how to set up \( S_0 \) so that addition and squaring can be interpreted as first-order properties of reachable configurations.

We interpret the number \( n \in \mathbb{N} \) as the configuration \((q, n, 0, 0)\) of the submonoid \( B \times \mathbb{B} \times \mathbb{N} \) of \( \mathit{MT} \). So we fix a distinguished control state \( q \). We will refer to configurations \((q, n_1, n_2, n_3)\) as \( q \)-configurations. For a \( q \)-configuration \( e = (q, n_1, n_2, n_3) \), let the projection \( \pi_i(e) = n_i \) return the value of the \( i \)-th counter with \( i \in \{1, 2, 3\} \). We write \( c_i \) and \( c_i \) for the increment and decrement operation on the \( i \)-th counter with \( i \in \{1, 2, 3\} \).

The valence system \( S_0 \) consists of the gadgets in Figs. 3 and 4 that we explain one by one. The first gadget to include in \( S_0 \) is depicted in Fig. 3. To be precise, we add a copy of these states and transitions for every combination of \( i, j, k \) such that \( \{i, j, k\} = \{1, 2, 3\} \). (The state \( q \) is the distinguished one. It is shared among the copies.) These transitions allow us to express useful auxiliary properties of \( q \)-configurations. In what follows, we implicitly require state\(_i\)(e) and/or state\(_a\)(d):

(i) We can express \( \pi_i(e) = \pi_i(d) \) with reach\((t_{i,j}, 0, 0, 0), e\).

(ii) We can express \( \pi_i(e) = 0 \) with reach\((t_{i,j}, 0, 0, 0), e\).

(iii) We can express \( \pi_i(e) = \pi_i(d) \) with \( \exists e : \text{reach}(t_{i,j}, 0, 0, 0, e) \land \text{state}_{e_{i}}(e) \land \text{reach}(e, e) \land \text{reach}(e, d) \).

(iv) We can express \( \pi_i(e) = \pi_i(d) \) with \( \exists e : \pi_i(e) = \pi_i(e) \land \pi_i(e) = \pi_i(e) \land \pi_i(e) = \pi_i(d) \).

(v) We can express \( \pi_i(e) \leq \pi_j(e) \) with reach\((t_{i,j}, 0, 0, 0), e\).

Figure 3. Gadget supporting auxiliary properties in \( S_0 \), for all \( i, j, k \) such that \( \{i, j, k\} = \{1, 2, 3\} \).

Note how all these formulas belong to the \( \Sigma_2 \) fragment of the logic. Now we are ready to show how to interpret first-order logic over the naturals.

**Domain** We represent the domain of the naturals with the configurations satisfying \( \text{state}_{q_{a}}(x) \land \text{state}_{q_{e}}(0) = 0 \land \text{state}_{q_{e}}(x) = 0 \).

**Addition** To represent addition, we include in \( S_0 \) the state \( a \) and the relevant portion of the gadget in Fig. 4, again sharing \( q \) with the rest of the construction. We can then express \( \pi_1(d) = \pi_1(e) + \pi_2(e) \) with the \( \Sigma_2 \) formula:

\[
\exists e_1, e_2, d' : \text{state}_{e_{1}}(e_1) \land \text{step}(e_1, c) \land \text{state}_{e_{2}}(e_2) \land \text{reach}(e_1, e_2) \land \text{state}_{d'}(d') \land \text{step}(e_2, d') \land \pi_2(d') = 0 = \pi_1(d) = \pi_1(e) + \pi_2(e).
\]

Note that \( e_2 \) is a \( q \)-configuration. It cannot be used for \( e_2 \). All that remains is to compare the value of the first counter in \( d \) to the first counter in \( d' \), again using one of the auxiliary predicates.

With the above, we can interpret \( \pi_1(e) = \pi_1(e) + \pi_1(d) \) in \( \Sigma_1 \) with the formula \( \exists e' : \pi_1(e') = \pi_1(e) \land \pi_2(e') = \pi_1(d) \land \pi_1(e) = \pi_1(e') + \pi_2(e') \).

**Squaring** To interpret squaring we add the transitions in Fig. 4 to \( S_0 \). We then express squaring in two steps.

First, we express \( \pi_1(d) \leq \pi_1(e)^2 \). We rely on the fact that \( n^2 = \sum_{i=0}^{n-1} 2i + 1 \). With this equation, it is sufficient to reach all configurations \((q, 0, 0, n')\) where \( n' \leq \sum_{i=1}^{n-1} 2(n - i) + 1 \). Indeed, the desired inequality can now bephased as \( \pi_1(d) \) being one of the values \( n' \). To compute the sum, we define a configuration \( c' \), using the interpretation for addition above, such that \( c' = (q, 2n - 1, 0, 0) \). We write this property as init\((c, c')\).

The trick for making sure that \( n' \leq \sum_{i=1}^{n-1} 2(n - i) + 1 \) is the following. We start with the value \( 2n - 1 \) in the first counter. We think of the counter values as tokens that can be moved, so that we have \( 2n - 1 \) tokens in the first component. Intuitively, we move the counter value back and forth between the first and the second component. This happens between the states \( m_3 \) and \( m_5 \). However, each time we move the tokens from one component to the other, we lose 2 tokens. Moreover, each time we move one token from the first to the second component, we increment the third. Now if we could make sure to move all tokens in every iteration, we would end up with counter value \( \sum_{i=1}^{n-1} (2i - 1) = n^2 \) in the third component. This we cannot guarantee, but we know that each time we take at most the tokens that are there (recall that the first two components
of our storage are $\mathbb{B}$, i.e., partially blind counters). Hence, we end up with counter value at most $n^3$ in the third component.

We return to our formula. Starting in configuration $(m,0,0,0)$ we increment the first counter to value $n_1$ and move to $c_1 = (m_1, n_1, 0, 0)$. We have to ensure that $n_1 = 2n - 1$. To this end, we require the existence of a path from $c_1$ via $m_2$ to a configuration $c'$ that satisfies init$(c,c')$. That the path is via $m_2$ guarantees $n_1 = \pi_1(c')$. That the init predicate holds yields $\pi_1(c') = 2n - 1$. Together, this is the equality we need.

We have to do the summation. From $c_1 = (m_1, n_1, 0, 0)$, we go to $c_3 = (m_3, n_1, 0, 0)$ from which we execute the $m_3$-$m_5$-loop. Eventually, we leave the loop and get to $d' = (q, \ldots, n')$. Since this is a $q$-configuration, we can use the auxiliary predicates and require $\pi_1(d) = \pi_1(d')$. The above argumentation on the construction of the gadget together with the fact that $n_1 = 2n - 1$ guarantees that we leave the loop with $n' \leq \sum_{i=1}^{m} n(n-1) + 1 = n^3$.

Formally, we interpret $\pi_1(d) \leq \pi_1(e)^2$ with the $\Sigma_1$ formula $\varphi_{\leq 0} (c,d)$ defined as follows:

$$3c' d' c_1 c_2 c_3 : \text{init}(c', c') \land \text{state}_g(d') \land \pi_1(d) = \pi_1(d') \land \text{reach}((m,0,0,0), c_1) \land \text{state}_m(c_1) \land \text{step}(c_1, c_2) \land \text{state}_m(c_2) \land \text{step}(c_2, c') \land \text{state}_m(c_3) \land \text{reach}(c_3, d').$$

In Appendix E, we elaborate on the correctness of the encoding.

To complete the construction we express $\pi_1(d) = \pi_1(e)^2$ in $\Pi_1$. We state that $d$ is the $q$-configuration with the maximal value in the first counter that is below $\pi_1(e)^2$. Formally, for every $q$-configuration $c$ we either have $\pi_1(e) > \pi_1(c)^2$ or $\pi_1(e) \leq \pi_1(d)$. The corresponding $\Pi_1$ formula is

$$\varphi_{\leq 0}(c,d) := \varphi_{\leq 0}(c,d) \land V c: \neg \varphi_{\leq 0}(c,e) \lor \pi_1(e) \leq \pi_1(d).$$

Formula $\varphi_{\leq 0}(c,d)$ is the needed interpretation of $\pi_1(d) = \pi_1(e)^2$.

If we now take a $\Sigma_1$ formula over $\langle N, +, \cdot \rangle$ and express addition as above and multiplication via $\varphi_{\leq 0}(\cdot, \cdot)$ and the identity $2ab = (a+b)^2 - a^2 - b^2$, we arrive at a $\Sigma_2$ formula. On the whole, we obtain the following result.

**Lemma 4.6.** Assume $\Gamma$ is not $\mathbb{B}^2$-triangle-free and construct the (fixed) valence system $S_{\Theta}$ over $M^1$ above. For each first-order formula $\varphi$ over the naturals with addition and multiplication, we can produce a $\varphi'$ over the configuration graph so that

$$(N, +, \cdot) \models \varphi \iff S_{\Theta} \models \varphi'.$$

Moreover, if $\varphi$ belongs to the $\Sigma_1$ fragment, then $\varphi'$ is in $\Sigma_2$.

To deduce undecidability of the $\Sigma_2$ fragment of FO[$R$] for a fixed valence system, note that the $\Sigma_1$ fragment of arithmetic with addition and multiplication is undecidable (Matiyasevich 1993). This concludes the proof of Theorem 4.5.

5. Discussion and Future Work

We provided a sufficient and necessary condition on a graph $\Gamma$ such that $\text{FO}[$$R$] for valence systems over the monoid $M^1$ is decidable. This result generalizes previous results on verification of infinite-state systems and provides a full characterization of the shape of the storage mechanisms enjoying decidability of $\text{FO}[$$R$]. The techniques employed in the proofs are robust enough to support extensions. For example, the results would still hold when adding a finite input alphabet $X$ to valence systems and adding labeled predicates step$_R$ with $x \in X$ and reach$_R$($\cdot$, $\cdot$) for a regular language $R \subseteq X^*$. As future work, it could be interesting to study whether similar characterizations exist for ordinary first-order logic or richer branching-time logics, such as the modal $\mu$-calculus.

References


A. Proof of Theorem 3.2

Suppose the monoid $M$ has an encoding $\theta : L \to R_3(M)$, $L \subseteq Y^*$, with automatic rational multiplication and let $S = (Q, M, E)$ be a valence system over $M$. Since $M$ is finitely generated, we have a surjective morphism $[\cdot] : X^* \to M$.

The reachability structure for $S$ has the domain $D = Q \times R_1(M)$, so we assume $Q \cap Y = \emptyset$ and take the regular language $QL = \{qx \mid q \in Q, x \in L\}$ to represent $D$. As expected, the bijection is then $\eta : QL \to D$ with $\eta(qx) = (q, \theta(x))$, where $q \in Q$ and $x \in L$. We have to verify that each of the relations in the reachability structure have regular encodings.

(i) The state,($\cdot$) predicate. Clearly, the set of its encodings is $QL$, which is regular.

(ii) The reachability predicate reach($\cdot$,$\cdot$). For each pair $p, q \in Q$, the set $R_{pq}$ of all $m \in M$ such that $(p, 1) \to_p^* (q, m)$ is a rational set: Take the automaton $A$ with state set $Q$ and whenever there is an edge $(r, m, s)$ in $S$, with $m = [w]$, create an edge labeled $w$ between $r$ and $s$. This automaton clearly satisfies $[\mathcal{L}_{pq}(A)] = R_{pq}$, so that $R$ is rational. The word relation corresponding to reach($\cdot$,$\cdot$) can now be written as

$$\bigcup_{p, q \in Q} \{(px, qx) \in QL \times QL \mid \exists r \in R_{pq} : \theta(x)r = \theta(y)\} = \bigcup_{p, q \in Q} \{(px, qx) \in QL \times QL \mid (\theta(x), \theta(y)) \in R_{pq}\} = \bigcup_{p, q \in Q} \{(p, q) : R_{pq}^\circ\}$$

which is regular (Fact 3.11).

(iii) The one-step reachability relation step($\cdot$,$\cdot$). Here, we can proceed as in the previous case, except that instead of the above $R_{pq}$, we take the finite set of all $m \in M$ for which there is a transition $(p, m, q)$ in $S$. Note that every finite set is rational.

(iv) Constants. Again, we use the fact that one-element subsets of $M$ are rational. Hence, given a state $q \in Q$ and a word $w \in X^*$ such that $[w] \in R_2(M)$, we can clearly compute the word $x \in L$ such that $\theta(x) = [w]$ and thus $\eta(qx) = (q, [w])$.

B. Proof of Lemma 3.10

Proof. The “if” statement can be checked straightforwardly. We prove the “only if” direction. Let $i \in [0, \min(m, n)]$ be maximal with the property that for all $j \in [1, i]$, we have $[u_{m-j+1}v_j] = 1$. We distinguish three cases.

- Suppose $i = \min(m, n)$ and $m \leq n$. We claim that we are in situation (i) above. The first condition is clearly satisfied. Moreover, the equations $[u_{m-j+1}v_j] = 1$ for $j \in [1, i]$ mean that

$$[u_1 \cdots u_k] = [vu] = [u_1 \cdots u_m v_1 \cdots v_n] = [v_{m+1} \cdots v_n].$$

Hence, Fact 3.8 yields the second and third condition.

- Suppose $i = \min(m, n)$ and $m > n$. We claim that we are in situation (ii) above. This can be shown analogously to the second case.

- Suppose $i < \min(m, n)$. We claim that we are in situation (iii) above. The first three conditions are clearly met. Furthermore, the equations $[u_{m-j+1}v_j] = 1$ for $j \in [1, i]$ imply that

$$[u_1 \cdots u_k]\neq [vu] = [u_1 \cdots u_m v_{i+1} \cdots v_n]$$

and since $[u_{m-i}v_{i+1}] \neq 1$ by maximality of $i$, the word $u_1 \cdots u_{m-i}v_{i+1} \cdots v_n$ is reduced. According to Fact 3.8, this entails the last three conditions.

C. Proof of Lemma 3.12

We begin with the identity $R^e = \sigma(K)$. Suppose $w \in R^e$ and let $w = u_1 \cdots u_n$ be the block decomposition. Then $\theta(w) = \theta(u_1)\cdots\theta(u_{n-1})\theta(u_n)$ and $\theta(u_i) \neq 1$ for $i \in [1, n]$. This means there is a word $v \in X^*$ with block decomposition $v = v_1 \cdots v_n$ such that $[v_i] = \theta(u_i)$ for $i \in [1, n]$. Note that $v$ is reduced. Since $[v] = \theta(w) \in R$, there is a word $u \in \mathcal{L}(A)$ with $[v] = [u]$. Because of our saturation, we may assume that $u$ is reduced. If $w = u_1 \cdots u_{m}$ is its block decomposition, then this implies $m = n$ and $[u_i] = [v_i]$ for $i \in [1, n]$. We distinguish two cases.

- If $n = 0$, then we have $[v] = [u] = 1$ and thus $w = \theta^{-1}([v]) = \varepsilon$. On the other hand, this means $1 \in R$ and therefore $\varepsilon \in K$. Thus, we have $w = \varepsilon = \sigma(K)$.

- Suppose $n > 0$ and consider a computation of $A$ that reads $w = u_1 \cdots u_n$,

$$q_0 \xrightarrow{u_1} q_1 \xrightarrow{u_2} \cdots \xrightarrow{u_n} q_n.$$ 

Moreover, let $i_j \in \{0, 1\}$ be the number with $u_j \in X^*_i$ for $j \in [1, n]$. Then, clearly, the word $x = (q_{i_1}, i_1, q_1)(q_{i_2}, i_2, q_2)\cdots(q_{i_{n-1}}, i_{n-1}, q_{n-1})(q_{i_n}, i_n, q_n)$ is contained in $K$. Moreover, since $[u_i] = [v_i] = \theta(u_i)$ and $[u_j] \in [\mathcal{L}_{pq}(A)] = R_{pq}$, we have $w = u_1 \cdots u_n$ is accepted by $A$ and satisfies $[u] = \theta(w)$. Since this means $\theta(w) = [u] \in R$, we have $w \in R^e$. This completes the proof of $R^e = \sigma(K)$.

We now turn to the identity $R^\circ = \tau(K^{rev})$. Let $w \in R^e$ and let $w = u_1 \cdots u_m$ be the block decomposition. Let $\theta(w) = \theta(u_1)\cdots\theta(u_{m-1})\theta(u_m)$ and $\theta(u_i) \neq 1$ for $i \in [1, n]$. This means there is a word $v \in X^{rev}$ with block decomposition $v = v_1 \cdots v_n$ such that $[v_i] = \theta(u_i)$ for $i \in [1, n]$. Note that $v$ is reduced. Since there is an $r \in R$ with $[v]r = [\theta(w)r] = 1$, there is a word $u \in \mathcal{L}(A)$ with $[v] = r$ and thus $[vu] = [v][u] = 1$. Since we saturated $A$, we may assume that $u$ is reduced. Let $u = u_1 \cdots u_m$ be its block decomposition. Now we have $u \in R$ and reduced and we know $[vu] = 1 = [\varepsilon]$. The words $v$ and $u$ clearly have to be merging, since otherwise the equality $[vu] = [\varepsilon]$ would contradict Fact 3.8. We can therefore apply Lemma 3.10. Note that the only case where the resulting word can be empty is case (i). This implies that $m = n$ and $[v_{m-j}u_j] = 1$ for all $j \in [1, n]$. As above, we distinguish two cases.

- If $n = 0$, then we have $[v] = [u] = 1$ and thus $w = \theta^{-1}([v]) = \varepsilon$. On the other hand, this means $1 \in R$ and therefore $\varepsilon \in K^{rev}$. Thus, we have $w = \varepsilon \in \tau(K^{rev})$.

- Suppose $n > 0$ and consider a computation of $A$ that reads $u = u_1 \cdots u_n$,

$$q_0 \xrightarrow{u_1} q_1 \xrightarrow{u_2} \cdots \xrightarrow{u_n} q_n.$$ 

Moreover, let $i_j \in \{0, 1\}$ be the number with $u_j \in X^*_i$ for $j \in [1, n]$. Then, clearly, the word $x = (q_{i_1}, i_1, q_1)(q_{i_2}, i_2, q_2)\cdots(q_{i_{n-1}}, i_{n-1}, q_{n-1})(q_{i_n}, i_n, q_n)$ is contained in $K^{rev}$. Moreover, since $\theta(u_{n-j+1})[u_j] = [v_{m-j+1}][u_j] = 1$ and $[u_j] \in [\mathcal{L}_{pq}(A)] = R_{pq}$, we have $w_{n-j+1} \in \tau((q_{j-1}, i_j, q_j))$ for all $j \in [1, n]$ (recall that $w_{n-j+1} \neq \varepsilon$ because $\theta(w_{n-j+1}) \neq 1$). In other words, we have $w_j \in \varepsilon$. 

$\square$
\[ \tau((q_{n-j}, i_{n-j+1}, q_{n-j+1}) \text{ for all } j \in [1, n]). \] This means \( w = w_1 \cdots w_n \in \tau(x) \subseteq \tau(K_{\text{rev}}). \)

This proves \( R^2 \subseteq \tau(K_{\text{rev}}). \) Now suppose \( w \in \tau(K_{\text{rev}}). \) Again, \( w = \varepsilon \) clearly implies \( \varepsilon \in K \) and hence \( 1 \in R \) and thus \( w = \varepsilon \in R^2. \) Suppose \( w \neq \varepsilon. \) Then there is an \( x = (q_{n-1}, i_n, q_n)(q_{n-2}, i_{n-1}, q_{n-1}) \cdots (q_0, i_1, q_1) \) such that \( w = w_1 \cdots w_n \) with

\[ w_j \in \tau((q_{n-j}, i_{n-j+1}, q_{n-j+1})). \]

This implies that for each \( j \in [1, n], \) there is a

\[ u_j \in L_{q_{n-j}, i_{n-j+1}}(A|i_{n-j+1}) \]

with \( \theta(w_j)[u_j] = 1. \) Therefore, if we set \( u = u_n \cdots u_1, \) then we have \( u \in \mathcal{L}(A) \) and thus \( [u] \in R. \) Hence

\[ \theta(w)[u] = \theta(u_1) \cdots \theta(u_n)[u_n] \cdots [u_1] = 1 \]

and thus \( w \in R^2. \) This completes the proof of \( R^2 = \tau(K_{\text{rev}}). \)

\section*{D. Proof of Theorem 3.7}

It remains to be shown that \( R^\circ = T_1 \cup T_2 \cup T_3 \cup T_4. \) We begin with the inclusion \( \subset \subseteq \) so we assume that \( x, y, z \in L \) with \( \theta(x) \theta(y) = \theta(z) \) for some \( \theta(x) \in R. \) We want to show that \( (x, z) \in T_1 \cup T_2 \cup T_3 \cup T_4. \) There are reduced words \( u, w \in \mathcal{L}^* \) and \( v \in \mathcal{L}(A) \) with \( [u] = \theta(x), [v] = \theta(y), \) and \( [w] = \theta(z) \) (recall that \( A \) is saturated). Let

\[ u = u_1 \cdots u_m, \quad x = x_1 \cdots x_m, \]

\[ v = v_1 \cdots v_n, \quad y = y_1 \cdots y_n, \]

\[ w = w_1 \cdots w_k, \quad z = z_1 \cdots z_k, \]

be block decompositions (note that since \( u, v, \) and \( w \) are reduced, they have the same number of blocks as \( x, y, \) and \( z, \) respectively). Then we have \( [u] = \theta(x), [x] = \theta(y), \) and \( [w] = \theta(z) \) for all possible indices \( i. \) Since \( v \in \mathcal{L}(A) \), there is a computation

\[ q_0 = v_1 \cdots v_k \}

\[ \theta(x) \theta(y) = \theta(u_1) \cdots \theta(u_n)[v] = [z] = \theta(x) \]

which confirms \( (x, z) \in R^\circ. \)

Now assume \( (x, z) \in T_2. \) Then there is an \( \ell \in \{0, 1\} \) and \( p \in \mathcal{Q} \) such that \( x \in I_{\ell,p,q}, \) and \( z \in W_{\ell,q,1}. \) This means \( \theta(x) \theta(y) = \theta(v_1) \cdots \theta(v_m) \theta(q) \theta(y) \theta(y) \theta(z) \) for some \( \ell \in \{0, 1\}, \) \( z \neq \varepsilon \) and

\[ \theta(x)[v] = \theta(z) \]

where \( \theta(x)[v] \) is the reduced word \( [v] = \theta(z) \)

and thus \( (x, z) \in R^\circ. \)

Suppose \( (x, z) \in T_3. \) Then we can decompose \( x = x'x'' \) such that \( x' \in I_{\ell,1}, \) and \( x'' \in W_{\ell,q,1}. \) The latter means that there is a \( r \in R \) with \( \theta(x') = \theta(x')r = 1. \) Moreover, the last block of \( z \) and the first block of \( x' \) are of different types; or one of the words is empty. This implies \( \theta(x/x') = \theta(z) \theta(x') \) and hence

\[ \theta(x'^r) = \theta(x) \theta(x') = 1 \]

meaning \( (x, z) \) belongs to \( R^\circ. \)

Finally, let \( (x, z) \in T_4. \) Then there are \( \ell \in \{0, 1\}, \) \( p, q \in \mathcal{Q} \), and \( r \in R^\circ \) such that \( x = x_1x_2x_3 \) and \( z = z_1z_2z_3 \) satisfy \( x_1 \neq x_2 \neq x_3 \) and \( z_1 \neq z_2 \neq z_3 \) for some \( x, y, z \in R^\circ \). Moreover, the last block of \( x \) and the first block of \( x' \) are zero-blocks and we know that \( x_1 \) and \( x_1' \) are not non-merging for \( i \in \{1, 2\}, \) and that \( \theta(x_1) \neq \theta(z_1) \) and \( \theta(z_2) \neq \theta(z_3) \) for \( \ell \in \{1, 2\}. \)

We want to construct an \( r \in R \) such that \( \theta(x') = \theta(x) \) for all \( \ell \neq \theta(x') \) in \( \mathcal{R}, \) we shall construct an encoding \( r = \tau(z) \) such that in this decomposition, each factor is non-merging with the next. Since
$x_3 \in R_{q_0,p}^\circ$, there is a $y_1 \in R_{q_0,p}^\circ$ with $\theta(x_3) \theta(y_1) = 1$. Observe that $x_3$ is either empty or begins with a $(1 - \ell)$-block. Hence, $y_1$ is either empty or ends with an $1 - \ell$-block. Since $(x_2, z_2) \in R_{p,q}^\circ$, we can find a $y_2 \in R_{p,q}^\circ$ with $\theta(x_2) \theta(y_2) = \theta(z_2)$. This means $\theta(y_2) \in M_1$, so that $y_1$ and $y_2$ are non-merging. Moreover, $z_3$ is empty or begins with a $(1 - \ell)$-block, so that also $y_2$ and $z_3$ are non-merging.

We have seen that $y_1 \in R_{q_0,p}^\circ, y_2 \in R_{p,q}^\circ$, and $z_3 \in R_{q,f}^\circ$.

Together with the non-merging relationships, this implies

$\theta(y_1 y_2 z_3) = \theta(y_1) \theta(y_2) \theta(z_3) \in R$

and furthermore

$\theta(x) \theta(y) = \theta(x_1 x_2 x_3) \theta(y_1 y_2 z_3)$
$= \theta(x_1) \theta(x_2) \theta(x_3) \theta(y_1) \theta(y_2) \theta(z_3)$
$= \theta(x_1) \theta(x_2) \theta(y_2) \theta(z_3)$
$= \theta(x_1) \theta(z_2) \theta(z_3)$
$= \theta(z_1) \theta(z_2) \theta(z_3)$
$= \theta(z)$,

confirming $(x, z) \in R^\circ$. This proves $R^\circ = T_1 \cup T_2 \cup T_3 \cup T_4$.

E. Correctness of squaring gadget

Let $c_1 = (m_1, 2n_1 - 1, 0, 0)$, as guaranteed by init$(c, c')$, step$(c_1, c_2)$, and step$(c_2, c')$. Any run going from $c_1$ to a $q$-configuration passing through an $m_2$-configuration will be of the following shape: After a first transition $c_1 \rightarrow (m_3, 2n - 1, 0, 0)$, there will be a number of executions of the $m_3$-$m_3$-loop. At the $i$-th execution, the self loops at $m_3$ and $m_5$ are fired $k_i$ and $h_i$ times, respectively. Let us write the configurations before the $i$-th $m_3$-$m_3$-loop as $(m_3, x_i, y_i, z_i)$. The cumulative effect of an execution of the $m_3$-$m_3$-loop is then $(c_1 c_2 c_3)^{k_i} c_2 c_2 (c_2 c_1)^{h_i}$, with $k_i \leq x_i$ and $h_i \leq y_i + k_i - 2$. We obtain that $x_{i+1} + y_{i+1} = x_i + y_i - 2$, so $x_{i+1} \leq x_i + y_i - 2$. The consequence of this estimation is that (i) the maximum number of iterations is given by $\lfloor (x_0 + y_0) / 2 \rfloor$ and (ii) at each iteration we add $k_i$ (so, at most $x_i$) to $z_i$. Hence, starting from $(m_3, 2n - 1, 0, 0)$ ($x_0 = 2n_1 - 1, y_0 = 0, z_0 = 0$, and thus $\lfloor (x_0 + y_0) / 2 \rfloor = n - 1$) we can reach precisely those $q$-configurations $d'$ where $\pi_3(d')$ is any number between 0 and $n^2$. 